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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

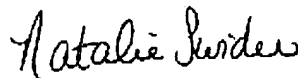
Applicant: Schwalbe, et al. Docket No.: INF-119
Serial No: 10/724,141 Art Unit: 2812
Date Filed: December 1, 2003
Title: Method for Patterning Dielectric Layers on Semiconductor Substrates

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Respectfully submitted,



Natalie Swider
Legal Assistant

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